1200 V SiC MPS[™] Diode

Silicon Carbide Power Schottky Diode

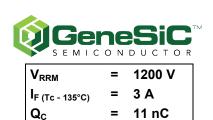
Features

- High Avalanche (UIS) Capability
- Enhanced Surge Current Capability
- 175 °C Maximum Operating Temperature
- Temperature Independent Switching Behavior
- Positive Temperature Coefficient Of V_{F}
- Extremely Fast Switching Speeds
- Superior Figure of Merit Q_C/I_F

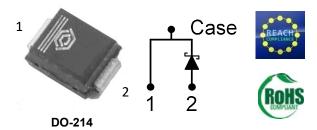
Advantages

- Low Standby Power Losses
- Improved Circuit Efficiency (Lower Overall Cost)
- Low Switching Losses
- Ease of Paralleling Devices without Thermal Runaway
- Smaller Heat Sink Requirements
- Low Reverse Recovery Current
- Low Device Capacitance
- Low Reverse Leakage Current at Operating Temperature

Absolute Maximum Ratings



Package



Applications

- Power Factor Correction (PFC)
- Switched-Mode Power Supply (SMPS)
- Solar Inverters
- Wind Turbine Inverters
- Motor Drives
- Induction Heating
- Uninterruptible Power Supply (UPS)
- High Voltage Multipliers

Parameter	Symbol	Conditions	Values	Unit	
Repetitive Peak Reverse Voltage	V _{RRM}		1200	V	
		$T_{C} = 25 \ ^{\circ}C, D = 1$	7		
Continuous Forward Current	I _F	T _c = 135 °C, D = 1	3	A	
		T _C = 158 °C, D = 1	2		
Non-Repetitive Peak Forward Surge Current,	1	T _C = 25 °C, t _P = 10 ms	21	^	
Half Sine Wave	I _{F,SM}	$T_{\rm C}$ = 150 °C, $t_{\rm P}$ = 10 ms	17	A	
Repetitive Peak Forward Surge Current, Half	I	T _C = 25 °C, t _P = 10 ms	14	А	
Sine Wave	I _{F,RM}	T _C = 150 °C, t _P = 10 ms	8	A	
Non-Repetitive Peak Forward Surge Current	I _{F,max}	T _C = 25 °C, t _P = 10 μs	220	А	
l ² t Value	∫i² dt	T _C = 25 °C, t _P = 10 ms	1.8	A ² s	
Non-Repetitive Avalanche Energy	E _{AS}	L = 30 mH, I _{AV} = 2 A, V _{DD} = 60 V	30	mJ	
Diode Ruggedness	dV/dt	V _R = 0 ~ 960 V	100	V/µs	
Power Dissipation	P _{tot}	T _C = 25 °C	39	Ŵ	
Operating and Storage Temperature	T _j , T _{stg}		-55 to 175	°C	

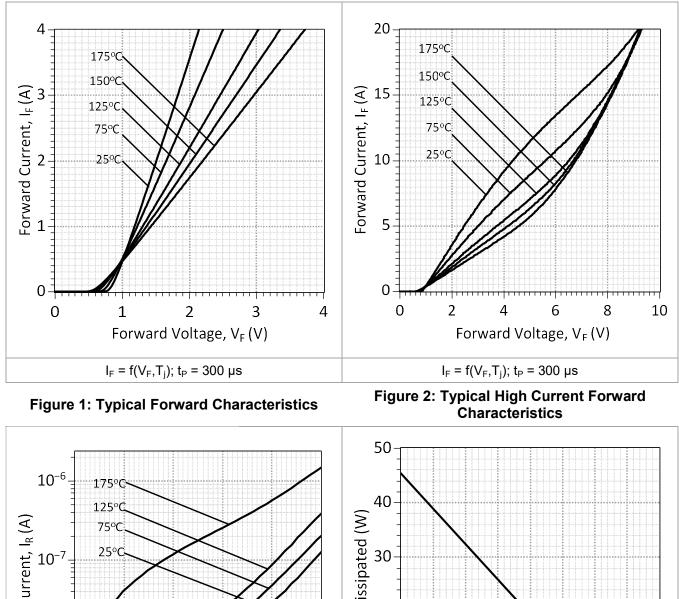
Electrical Characteristics

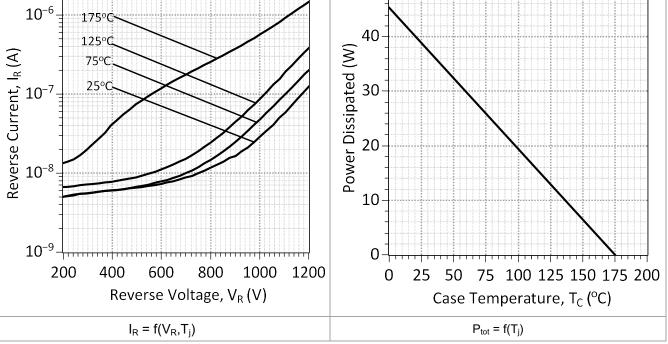
Parameter	Quara had	Conditions		Values			11
Parameter	Symbol			min.	typ.	max.	ax. Unit
Diode Forward Voltage	V _F	I _F = 2 A, T _j = 25 °C I _F = 2 A, T _i = 175 °C			1.5 2.3	1.8 2.7	V
Reverse Current	I _R	,	V _R = 1200 V, T _j = 25 °C V _R = 1200 V, T _i = 175 °C		0.1 1	2 19	μA
Total Capacitive Charge	Qc	$I_{F} \leq I_{F,MAX}$	V _R = 400 V V _R = 800 V		7 11		nC
Switching Time	t _s	dl _F /dt = 200 A/µs T _j = 175 °C	V _R = 400 V V _R = 800 V		< 10		ns
Total Capacitance	С	V _R = 1 V, f = 1 MHz, T _j = 25 °C V _R = 800 V, f = 1 MHz, T _i = 25 °C		118 8		pF	

Thermal Resistance, Junction - CaseR3.86°C/W









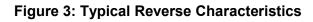
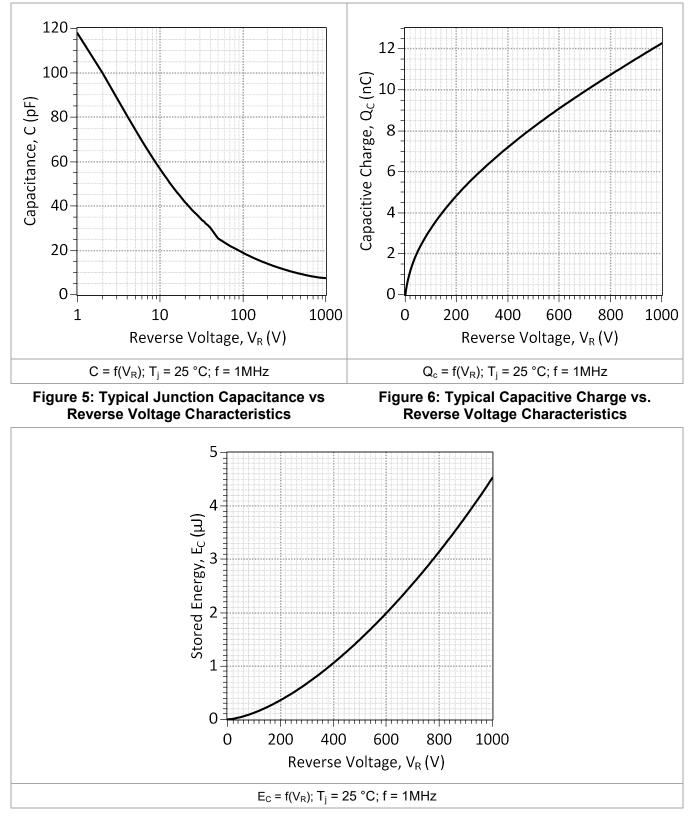
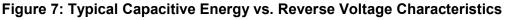


Figure 4: Power Derating Curve



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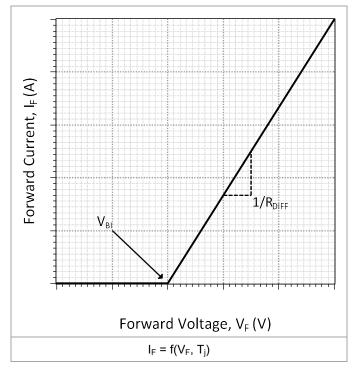


Figure 8: Forward Curve Model



 $I_F = (V_F - V_{BI})/R_{DIFF}$

Built-In Voltage (V_{BI}): $V_{BI}(T_i) = m^*T_i + b,$ m = -1.30e-03, b = 0.906

Differential Resistance (RDIFF):

 $R_{DIFF}(T_j) = a^*T_j^2 + b^*T_j + c (\Omega);$ a = 5.98e-05, b = 8.58e-03, c = 1.96

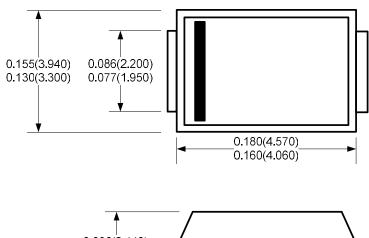
1200 V SiC MPS™ Diode

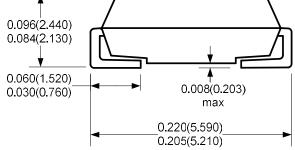
Package Dimensions:



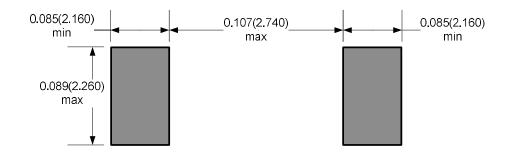
DO-214

PACKAGE OUTLINE





Recommended Solder Pad Layout



NOTE

- 1. CONTROLLED DIMENSION IS INCH. DIMENSION IN BRACKET IS MILLIMETER.
- 2. DIMENSIONS DO NOT INCLUDE END FLASH, MOLD FLASH, MATERIAL PROTRUSIONS

1200 V SiC MPS™ Diode

RoHS Compliance



The levels of RoHS restricted materials in this product are below the maximum concentration values (also referred to as the threshold limits) permitted for such substances, or are used in an exempted application, in accordance with EU Directive 2011/65/EC (RoHS2), as implemented January 2, 2013. RoHS Declarations for this product can be obtained from your GeneSiC representative.

REACH Compliance

REACH substances of high concern (SVHCs) information is available for this product. Since the European Chemical Agency (ECHA) has published notice of their intent to frequently revise the SVHC listing for the foreseeable future, please contact a GeneSiC representative to insure you get the most up-to-date REACH SVHC Declaration. REACH banned substance information (REACH Article 67) is also available upon request.

This product has not been designed or tested for use in, and is not intended for use in, applications implanted into the human body nor in applications in which failure of the product could lead to death, personal injury or property damage, including but not limited to equipment used in the operation of nuclear facilities, life-support machines, cardiac defibrillators or similar emergency medical equipment, aircraft navigation or communication or control systems, or air traffic control systems.

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Related Links

- Soldering Document: http://www.genesicsemi.com/quality/quality-manual/
- Tin-whisker Report: http://www.genesicsemi.com/quality/compliance/
- Reliability Report: http://www.genesicsemi.com/quality/reliability/



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Rev1.1

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SPICE Model Parameters

This is a secure document. Please copy this code from the SPICE model PDF file on our website (http://www.genesicsemi.com/sic_rectifiers_diodes/merged_pin_schottky/GC02MPS12-214_SPICE.pdf) into LTSPICE (version 4) software for simulation of the GC02MPS12-214.

* Gene	SiC Sem:	iconducto	or SiC MPS™	Rectifier				
* Revision: 1.1								
* Date: February-2018								

* *	DO-214	package						

.SUBCKT GC02MPS12 A K Case								
L anode	A	AD	5n					
D1	AD	Case	GC02MPS12					
L_cathode	K	Case	5n					
.ends								

.SUBCKT GC02MPS12 ANODE KATHODE								
D1 ANODE KATHODE GC02MPS12_SCHOTTKY								
.MODEL GC02MPS12_SCHOTTKY D								
+ IS	2.05E-	15	RS	0.3105				
+ N	1		IKF	500				
+ EG	1.2		XTI	2				
+ TRS1	0.0054	34	TRS2	2.717E-05				
+ CJO	1.65E-	10	VJ	0.879				
+ M	0.438		FC	0.5				
+ TT	1.00E-	10	BV	1600				
+ IBV	0.2E-0	6	VPK	1200				
+ IAVE	2		TYPE	SiC_MPS TM				
+ MFG	GeneSi	C_Semi						
.ENDS								
* End of GC02MPS12-214 SPICE Model								

* This model is provided "AS IS, WHERE IS, AND WITH NO WARRANTY OF ANY KIND								
* EITHER EXPRESSED OR IMPLIED, INCLUDING BUT NOT LIMITED TO ANY IMPLIED								

* WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE."